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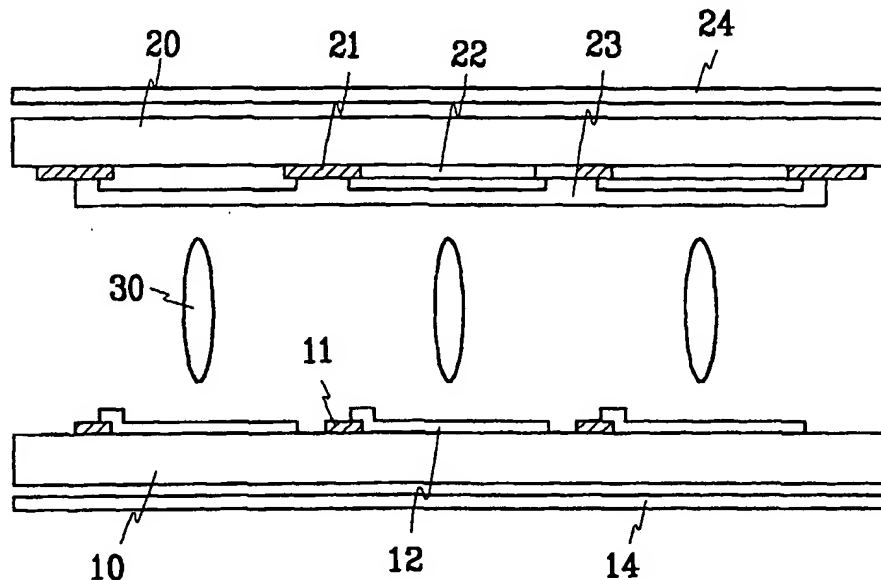
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(54) Title: HIGH-SPEED HIGH-TEMPERATURE NEMATIC LIQUID CRYSTAL COMPOSITION AND LIQUID CRYSTAL
DISPLAY COMPRISING THE SAME



(57) Abstract: The present invention relates to a high-speed high-temperature nematic liquid crystal composition and a liquid crystal display comprising the same, particularly to a high-speed high-temperature nematic liquid crystal composition which comprises a nematic liquid crystal compound having an isothionate group on the end aromatic ring and having one or more kinds of fluorine atoms, and thus satisfies a high response speed property and has a high phase transition temperature of liquid crystal, a low operating voltage, and a broad nematic phase temperature range, and a liquid crystal cell comprising the same, and a liquid crystal display using the same.

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**HIGH-SPEED HIGH-TEMPERATURE NEMATIC LIQUID CRYSTAL COMPOSITION
AND LIQUID CRYSTAL DISPLAY COMPRISING THE SAME**

BACKGROUND OF THE INVENTION

5 **(a) Field of the Invention**

The present invention relates to a high-speed high-temperature nematic liquid crystal composition and a liquid crystal display comprising the same, and particularly to a high-speed high-temperature nematic liquid crystal composition comprising a nematic crystal liquid compound that has a high phase liquid crystal transition temperature, large birefringence and elasticity coefficients, and a broad operating nematic phase temperature range, and thus can realize a high response speed and is effective for various devices requiring liquid crystal such as LCDs, etc., and a liquid crystal cell comprising the same and a liquid crystal display using the same.

15 **(b) Description of the Related Art**

Generally, a liquid crystal display is a device in which liquid crystal material is injected between an upper substrate on which opposite electrodes and color filters, etc. are formed, and a lower substrate on which a thin film transistor and pixel electrodes, etc. are formed, and wherein different electric potentials are applied to the pixel electrodes and the opposite electrodes to form an electric field to change an

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arrangement of liquid crystal molecules, thereby controlling transmissivity of light to embody images.

Liquid crystal displays are divided into TN (Twist Nematic), STN (Super Twist Nematic), and ferroelectric liquid crystal displays of the passive matrix method, and TFT (Thin Film Transistor), MIM (Metal Insulator Metal), and diode liquid crystal displays of the active matrix method, according to operation type.

For an active matrix method, high voltage maintenance with a low leak current is important because a liquid crystal display is operated with switching devices such as TFTs or MIMs for each pixel. In addition, a liquid crystal display tends to require a high response speed due to an increase in display information amount and for embodiment of moving pictures.

The thin film transistor substrate is used as a circuit substrate for independently operating each pixel in a liquid crystal display or an organic electroluminescence (EL) display, etc. On the thin film transistor substrate, scanning signal wiring or gate wiring for transmitting scanning signals, and picture signal lines or data wiring for transmitting picture signals are formed, and it comprises a thin film transistor connected with the gate wiring and data wiring, pixel electrodes connected with the thin film transistor, a gate insulating film that covers the gate wiring to insulate it, and a protection film that covers the thin film transistor

and the data wiring to insulate them. The thin film transistor comprises a semiconductor layer that forms a gate electrode and a channel, which are parts of gate wiring; a source electrode and drain electrode, which are parts of data wiring; and a gate insulating film and a protection film, etc. The thin film transistor is a switching device for transmitting or blocking picture signals transmitted through data wiring according to scanning signals transmitted through gate wiring.

In addition; a nematic liquid crystal composition is used as an important material for a liquid crystal display (LCD) used for electronic computers, electronic notebooks, personal computers, etc. And recently, it has widely spread as a display for high speed information treatment for word processors, personal computers, etc.

However, because nematic liquid crystal compositions known so far have a low response speed, it is difficult to adequately embody moving pictures with them.

In order to solve these problems, the following requirements should be satisfied. First, in order to improve a response speed, the viscosity of a liquid crystal material should decrease to a range of 20 to 25 mm²/s. Second, in order to lower an operating voltage, a dielectric anisotropy ($\Delta \epsilon$) thereof should increase to a range of 10 to 15 (35°C, 1kHz). Third, a liquid crystal material should have a nematic phase over a broad temperature range, and preferable the nematic phase temperature range is -30 to 80 °C. Forth, birefringence (Δn) should be 0.20 (25 °C)

or more.

In addition, although the LCD has physical advantages of lightness and smallness, brightness, which is one of various factors determining picture quality, is weak compared to a CRT (Cathode Ray Tube). Additionally, as the LCD-TV market is attracting attention as a large market for monitors, and new LCDs are being produced and disseminated, requirements for high brightness and high response speed become very important.

In order to achieve high brightness in an LCD, the phase transition temperature of liquid crystal should be maintained high due to a tube current in the back light. Also, for a high response speed, the rotation viscosity of the material should decrease or the refractive index of liquid crystal should increase.

Most LCD products marketed to date use nematic liquid crystal such as TN, IPS, and VA modes, and the phase transition temperature thereof is about 70 to 80 °C and response speed is about 20 to 30 ms. However, since the phase transition temperature and response speed are still unsatisfactory, improvements in response speed and increases in the phase transition temperature are needed in order to realize TV applications and moving pictures.

In addition, it is well known that a liquid crystal material having high birefringence (Δn) and elasticity coefficients is required in order to improve

electro-optical characteristics of the TN-LCD, STN-LCD, and TFT-LCD. However, although liquid crystal compounds so far known can improve electro-optical characteristics of liquid crystal material, problems still remain in chemical safety of liquid crystal material and the operating temperature range of a liquid crystal display because if the liquid crystal compounds are used, birefringence of the mixed liquid crystal increases but a smectic phase easily appears or the operating nematic phase temperature range is narrow.

SUMMARY OF THE INVENTION

The present invention is made in consideration of the problems of the prior art, and it is an object of the present invention to provide a novel nematic liquid crystal compound that does not largely increase operation voltage, that increases birefringence (Δn) and elasticity coefficients (K_{11} , K_{33}), and that broadens an operating nematic phase temperature range and thus makes response speed high, and a nematic liquid crystal composition comprising the same.

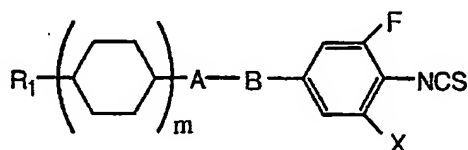
It is another object of the present invention to provide a nematic liquid crystal composition that has a high phase transition temperature of liquid crystal and that can realize a high response speed.

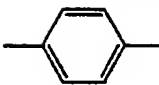
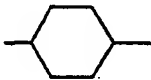
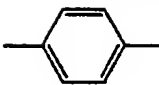
It is another object of the present invention to provide a liquid crystal cell comprising the above nematic liquid crystal composition, and a liquid crystal display

with improved electro-optical properties using the same.

In order to achieve these objects, the present invention provides a nematic liquid crystal composition comprising a nematic liquid crystal compound represented by the following Chemical Formula 1:

5 [Chemical Formula 1]



wherein R_1 is $\text{C}_n\text{H}_{2n+1}$ or $\text{C}_n\text{H}_{2n-1}$ (n is 1~15); X is H or F; A is  or  or ; B is $-\text{CH}_2-\text{CH}_2-$ or $-\text{C}\equiv\text{C}-$; and m is 0 or 1.

The present invention also provides a liquid crystal cell for a liquid crystal display in which the above nematic liquid crystal composition is injected as liquid crystal between two glass substrates or plastic substrates.

Specifically, the present invention provides a liquid crystal display comprising:

- a first substrate having an outside and an inside;
- 15 a second substrate opposed to the first substrate and having an outside and an inside;
- pixel electrodes formed on one of the insides of the first substrate and the second substrate;

common electrodes formed on one of the insides of the first substrate and the second substrate; and

liquid crystal cells in which the above nematic liquid crystal composition is injected as liquid crystal between the first substrate and the second substrate.

- 5 A 1-gradation voltage applied between the pixel electrode and the common electrode is preferably a value in a range within which a standardized comparison ratio becomes 0.8 or more in every view angle, when standardizing the comparison ratio for applying 0V of the 1-gradation voltage as 1.

- Additionally, the liquid crystal display preferably comprises a thin film
- 10 transistor array substrate comprising an insulating substrate; gate wiring formed on the insulating substrate, comprising gate lines and gate electrodes connected with the gate lines; a gate insulating film covering the gate wiring; a semiconductor layer formed on the gate insulating film; data lines formed on the semiconductor layer and crossed with the gate lines; source electrodes connected with the data lines; data
- 15 wiring comprising drain electrodes opposed to the source electrodes around the gate electrodes; and pixel electrodes connected with the drain electrodes, as the lower substrate.

BRIEF DESCRIPTION OF THE DRAWINGS

Fig. 1a is a cross-sectional view of the liquid crystal display according to a

first example of the present invention;

Fig. 1b is an arrangement view of the liquid crystal display according to the first example of the present invention wherein opening patterns of pixel electrodes and common electrodes are overlapped with each other;

5 Fig. 2 is an arrangement view of the structure of a thin film transistor substrate for a liquid crystal display according to a second example of the present invention;

Fig. 3 is a cross-sectional view along the V-V' line in Fig. 2;

 Figs. 4a, 5a, 6a, and 7a are arrangement view of thin film transistor
10 substrates in the middle of a process for preparing a thin film transistor substrate for a liquid crystal display according to the second example of the present invention;

Fig. 4b is a cross-sectional view along the line VIb-VIb' in Fig. 4a;

Fig. 5b is a cross-sectional view along the line VIIb-VIIb' in Fig. 5a and showing the next step of Fig. 4b;

15 Fig. 6b is a cross-sectional view along the line VIIIb-VIIIb' in Fig. 6a and showing the next step of Fig. 5b;

Fig. 7b is a cross-sectional view along the line IXb-IXb' in Fig. 7a and showing the next step of Fig. 6b;

Fig. 8 is an arrangement view showing the structure of a thin film transistor

substrate for a liquid crystal display according to a third example of the present invention; and

Fig. 9 is a cross-sectional view along the line XXI-XXI' in Fig. 8.

DETAILED DESCRIPTION AND THE PREFERRED EMBODIMENTS

5 The present invention will now be explained in detail.

The present invention is characterized by a nematic liquid crystal composition comprising a nematic liquid crystal compound of the above Chemical Formula 1 that lowers viscosity of liquid crystal, increases dielectric anisotropy and birefringence thereof, and broadens a nematic phase temperature range to make a
10 response speed high; and a liquid crystal cell using the same as liquid crystal material; and a liquid crystal display using the same that can realize high brightness and high response-speed technology.

First, the nematic liquid crystal composition used for liquid crystal material in the liquid crystal display of the present invention will be explained in detail.

15 The nematic liquid crystal composition of the present invention blends a compound of the above Chemical Formula 1 with a conventional commercial liquid crystal composition as a key material, thereby elevating a phase transition temperature by at least 10°C compared to the existing commercial liquid crystal, and realizing a response speed of approximately 10 ms.

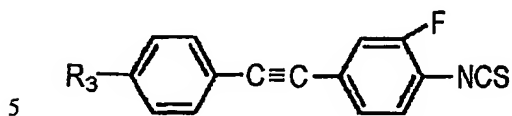
In addition, the nematic liquid crystal composition comprising a compound of the above Chemical Formula 1 as a key material has a significantly high birefringence (Δn) of 0.20 or more, and a very broad nematic phase temperature range of approximately 140 °C or more. If a liquid crystal compound selected from a group consisting of the compounds of the following Chemical Formulae 2 to 6, substituted by a fluorine atom and having a tolane group as a connection group added, the elasticity coefficient of the nematic liquid crystal composition increases to make the response speed high. In addition, since the compound of the above Chemical Formula 1 has very good compatibility with additive compounds, a nematic liquid crystal composition showing excellent properties can be obtained without sacrificing good properties of essential components.

The nematic liquid crystal composition of the present invention preferably comprises at least 2 kinds of liquid crystal compounds, one or more kinds of compounds selected from a group consisting of the compounds of the above Chemical Formula 1 as a key material, and one or more kinds of compounds selected from a group consisting of the compounds of the above Chemical Formula 2 as a key material.

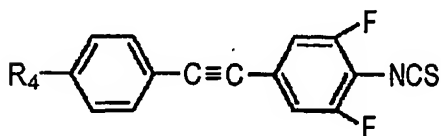
More preferably, the liquid crystal composition of the present invention further comprises 5 to 40 wt% of one or more kinds of compounds selected from a

group consisting of the following Chemical Formulae 2, 3, 4, and 5 with triple bonds, to increase birefringence (Δn) and the elasticity coefficient and to make the response speed much higher.

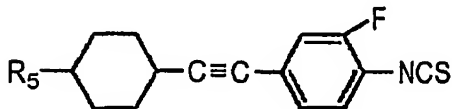
[Chemical Formula 2]



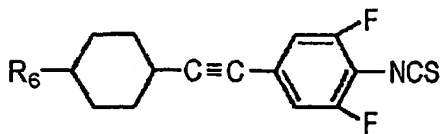
[Chemical Formula 3]



[Chemical Formula 4]



10 [Chemical Formula 5]



(wherein each of $R_5 \sim R_6$ is preferably a linear alkyl chain of C_nH_{2n+1} (n is an integer of 3~7) or a linear alkyl chain of $CH_5C_nH_{2n-2}$ (n is an integer of 2 to 6) having one double bond in the middle of an alkyl chain.)

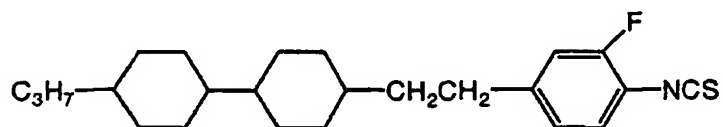
15 In addition, the liquid crystal composition of the present invention may use generally-known nematic liquid crystal, smectic liquid crystal, cholesteric liquid

crystal, etc. in addition to the liquid crystal compound of the above Chemical Formula 1 that is used to improve properties of a liquid crystal composition. However, since desired properties of a liquid crystal composition may decrease if such liquid crystal compound is added in a large amount, the added amount should be limitedly
5 determined according to desired properties of the nematic liquid crystal composition.

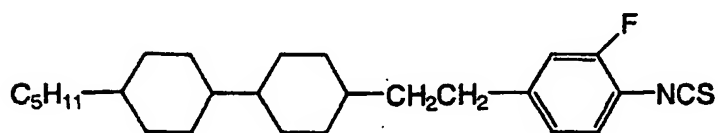
The nematic liquid crystal composition of the present invention preferably comprises the conventional nematic liquid crystal compound together with the nematic compound of the above Chemical Formula 1.

Preferably, as the compound of the above Chemical Formula 1, compounds
10 of the following Chemical Formulae 1a to 1k can be used alone or in combination.

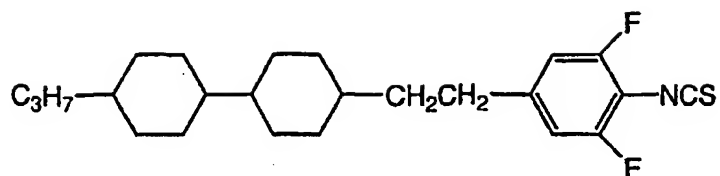
[Chemical Formula 1a]



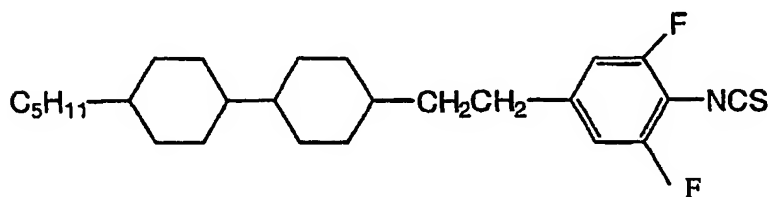
[Chemical Formula 1b]



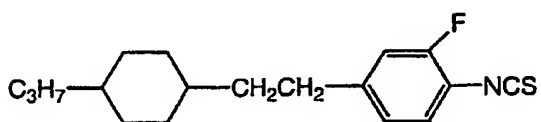
15 [Chemical Formula 1c]



[Chemical Formula 1d]

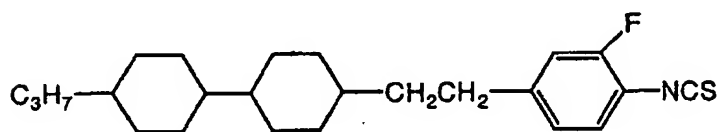


[Chemical Formula 1e]

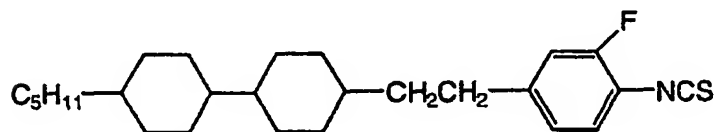


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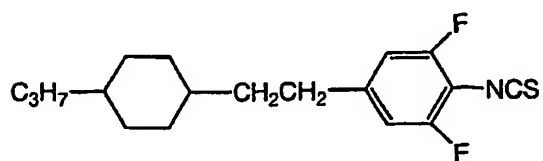
[Chemical Formula 1f]



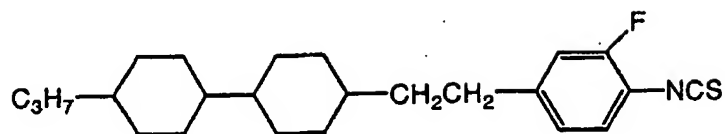
[Chemical Formula 1g]



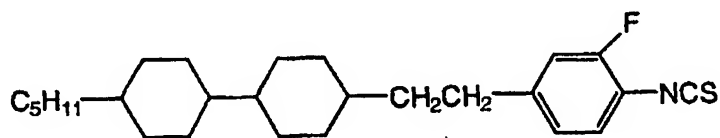
10 [Chemical Formula 1h]



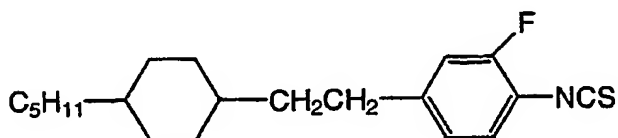
[Chemical Formula 1i]



[Chemical Formula 1j]



5 [Chemical Formula 1k]



It is more preferable to use the compound of the above Chemical Formula 1f and the compound of the Chemical Formula 1g together. In such a case, a mixing ratio of the compound of the Chemical Formula 1f and the compound of the Chemical

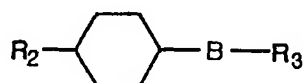
10 Formula 1g is preferably 1 to 80 : 1 to 80 by weight ratio. Additionally, it is more preferable to use the compounds of the Chemical Formulae 1i, the Chemical Formula 1j, and the Chemical Formula 1k together. In such a case, the mixing ratio of the compounds of the above Chemical Formulae 1i, 1j, and 1k is preferably 1 to 80 : 1 to 80 : 1 to 80 by weight ratio.

15 The compound of the above Chemical Formula 1 is preferably contained in an amount of 2 to 80 wt%, and if the used amount of the compound of the Chemical

Formula 1 exceeds this range, a high phase transition temperature and high response speed cannot be obtained.

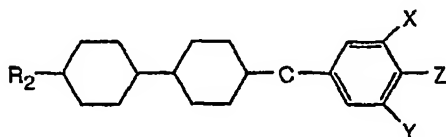
In addition, it is preferable to use at least one kind of commercial liquid crystal compound. The commercial liquid crystal compound is preferably one or more kinds selected from a group consisting of the compounds of the following Chemical Formulae 6, 7, and 8. The content of the commercial liquid crystal compound are preferably 20 to 98 wt%.

[Chemical Formula 6]

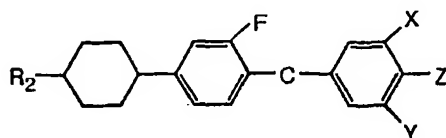


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[Chemical Formula 7]



[Chemical Formula 8]



wherein R_2 and R_3 are independently or simultaneously C1-15 alkyl or alkoxy groups; B is phenyl or cyclohexyl; C is a single bond $-CH_2CH_2-$ or $-COO-$; X and Y are independently or simultaneously a hydrogen or fluorine atom; and Z is a hydrogen, $-OCF_3$, or a fluorine atom.

As the commercial liquid crystal compound, the compounds of the Chemical Formulae 6, 7, and 8 can be used in combination, and for example, it is more preferable to divide the compounds of the Chemical Formulae 6 to 8 into 4 groups (G1 to G4) and use them together. The G1 to G4 preferably comprise 25 to 45 % of G1, 15 to 25 wt% of G2, 10 to 20 wt% of G3, and 15 to 30 wt% of G4. Preferably, G1 group compounds are at least two kinds of compounds selected from a group consisting of the compounds of the above Chemical Formula 6; G2 compounds are at least 2 kinds of compounds selected from a group consisting of the compounds of the above Chemical Formula 7 and 8; G3 compounds are at least 2 kinds of compounds selected from a group consisting of the compounds of the Chemical Formula 7; and G4 compounds are at least 2 kinds of compounds selected from a group consisting of the compounds of the Chemical Formulae 7 and 8. However, the mixing ratio is not limited to them, and variation can be made according to the kinds of the Chemical Formula 1.

The present invention also provides a liquid crystal cell comprising a nematic liquid crystal composition comprising the compound of the Chemical Formula 1.

The liquid crystal cell of the present invention comprises the above liquid crystal composition and thus has large birefringence, a high response speed, and superior cell parameters such as phase transition temperature. Preferably,

birefringence (Δn) is 0.2 or more. Also, the phase transition temperature is at least 85 °C and preferably 90 °C, and the nematic phase temperature range is -30 to 80 °C. Additionally, the response speed of the liquid crystal is preferably 9 to 11 ms.

5 The liquid crystal cell of the present invention consists of two glass substrates or transparent plastic substrates, and the nematic liquid crystal composition comprising the compound of the Chemical Formula 1 is injected therebetween.

Transparent electrodes making up pixels are located inside of the two glass
10 substrates, and an orientation film for orienting liquid crystal molecules toward one direction is located thereon. Between the glass substrates, a constant distance is maintained so that liquid crystal can be injected, and in the case of a display with a broad area, the distance is maintained by a spacer. Liquid crystal is injected in the space and oriented. On the side of the cell, an electrode pattern connected with the
15 transparent electrode is located, and an external voltage is transmitted to the liquid crystal through the pattern. On an active matrix liquid display, patterns for operating an active device are formed, and the active device is operated through the patterns and simultaneously an external voltage is applied to the liquid crystal.

In order to apply the liquid crystal cell for a liquid crystal display, a display

electrode pattern should be formed. There are two methods to achieve this: one is to place a mask punched with the same shape as the electrode to attach conductive films only in the same shape as the pattern; and the other is a photo-etching method used for making a much finer pattern, by printing an electrode pattern on a glass substrate to which an ITO film is attached in the same way as printing a film to dissolve the parts other than at the pattern with chemicals. The 2 glass substrate sheets are joined with a space of about 10 microns, and then liquid crystal is filled between the sheets. At this time, an empty cell is firstly assembled and its side is mostly sealed, and liquid crystal is injected through a small unsealed part using a capillary method, a method using a pressure difference, or a method for making a vacuum condition and injecting. The hole through which liquid crystal is injected is then sealed with adhesive to complete the liquid crystal cell. The prepared liquid crystal cell is inserted between 2 polarizing plate sheets, and a reflecting plate is laid down so as to give a light source to complete a liquid crystal panel for a display. A color filter between the liquid crystal cell and the polarizing plate can also be considered, and since the distance between the 2 sheets of the liquid crystal cells is approximately 10 microns and thus dust is likely to contaminate the circuit, it is preferable to manufacture the liquid crystal panel in a clean room completely equipped with dustproof equipment.

The present invention also provides liquid crystal displays of various LCD product groups using the nematic liquid phase composition as liquid crystal material by filling it in liquid crystal cells for various displays together with appropriate additives. Preferably, an active matrix method TFT liquid crystal display, an active matrix method MIM liquid crystal display, an active matrix method IPS (In-plane switching) liquid crystal display, a simple matrix type twist nematic liquid crystal display, a simple matrix type super twist nematic liquid crystal display, a thin film transistor-twist nematic (TFT-TN) liquid crystal display, an AOC (Array on color filter) liquid crystal display, or a COA (Color filter on array) liquid crystal display, etc. comprising the nematic liquid crystal composition can be manufactured.

In an active matrix liquid crystal display, an active device diode or transistor is formed on the side of the pixel on the lower substrate. A color liquid crystal display is equipped with a color filter so that color can be displayed on the upper substrate. The color filter is placed between the transparent electrode and the glass substrate, and it may be formed by a pigment spray method, a dyeing method, etc. on the glass substrate. A thus- prepared liquid crystal display of the present invention has a superior high speed response and high heat resistance, and has little afterimage and plica and thus it can be used under any circumstances.

Now, a liquid crystal display comprising the liquid crystal cell according to a

preferred example of the present invention will be explained in detail so that a person ordinarily skilled in the art can easily work it.

The liquid crystal display of the present invention comprises the lower substrate on which pixel electrodes having openings and thin film transistors are formed, the upper substrate on which common electrodes having openings are
5 formed, and a liquid crystal cell between the lower substrate and the upper substrate, oriented perpendicularly to the two substrates, in which a liquid crystal composition comprising the nematic liquid crystal compound of the above Chemical Formula 1 is injected as liquid crystal. On the outside of the upper substrate and the lower
10 substrate, the upper polarizing plate and the lower polarizing plate are respectively arranged with their polarizing directions perpendicular to each other.

Fig. 1a is a cross-sectional view of the liquid crystal display according to the first example of the present invention, and Fig. 1b is an arrangement view of the liquid crystal display according to the first example of the present invention wherein
15 opening patterns of pixel electrodes and common electrodes are overlapped.

The liquid crystal display comprises the lower substrate (10), the upper substrate (20) opposed to the lower substrate, and a liquid crystal cell (30) comprising the high-speed high-temperature liquid crystal material of the present invention as liquid crystal, which is injected between the lower substrate (10) and the

upper substrate (20) and oriented perpendicularly to the substrates (10, 20).

On the lower substrate (10) consisting of transparent insulating material such as glass, etc., pixel electrodes (12) consisting of a transparent conductive material such as ITO (indium tin oxide) or IZO (indium zinc oxide), etc. and having
5 opening patterns (not shown) are formed, each of which is connected to a switching device (11) to receive a picture signal voltage. As the switching device (11), a thin film transistor is commonly used, which is connected to a gate line (not shown) transmitting scanning signals, and a data line (not shown) transmitting picture signals and on and off signals for pixel electrodes (12) according to scanning signals.
10 Additionally, under the lower substrate (10), a lower polarizing plate (14) is attached. In the case of a reflection-type liquid crystal display, the pixel electrode (12) might not consist of a transparent material, and in such a case, the lower polarizing plate (14) is unnecessary.

Under the upper substrate (20), also consisting of a transparent insulating
15 material such as glass, etc., a black matrix (21) for preventing light from getting out; red, green, and blue color filters (22); and common electrodes (23) consisting of a transparent conductive material such as ITO or IZO, etc. and having an opening pattern (not shown) are formed. The black matrix (21) or color filters (22) may also be formed on the lower substrate (10). The upper polarizing plate (24) is attached

on the upper substrate (20).

The lower polarizing plate (14) and the upper polarizing plate (24) are arranged so that their polarizing directions are perpendicular to each other, and thus as an electric field applied to the liquid crystal layer becomes weaker, a normally
5 black mode where a picture is shown to be dark appears.

In the liquid crystal display, as means for regulating an inclined direction of liquid crystal molecules, an opening pattern is formed on common electrodes and pixel electrodes. The opening pattern will now be explained.

Referring to Fig. 1b, in the middle of the rectangular pixel electrode (12), a
10 first opening (121) narrowly dug from the right side to the left side is formed, and both corners of the first opening (121) are gently rounded. Dividing the pixel electrode (12) into an upper part and a lower part with respect to the area of the first opening (121), second and third openings (122, 123) are diagonally formed by digging into the upper part and the lower part of the pixel electrode (12). The second and the third
15 openings (122, 123) are formed to symmetrically angle away from the first opening (121).

On the common electrode, a fourth opening comprising a stem part (211) formed horizontally, first and second branch parts (212, 214) extending diagonally from the stem part (211) respectively in upward and downward directions, and first

and second branch end parts (213, 215) respectively extending from the first and the second branch parts (212, 214) vertically upward and downward. Additionally, on the common electrode, a fifth opening comprising a center part (221) formed in a slanted direction parallel with the first branch part (212), a horizontal end part (222) extending horizontally from the center part (221), and a vertical end part (223) extending vertically from the center part (221); and a sixth opening that is symmetrical to the fifth opening around the fourth opening, are formed. The fourth, fifth, and sixth openings with such arrangements are repeatedly formed on the common electrodes.

10 The first to third openings (121, 122, 123) of the pixel electrode (12) and the fourth to sixth openings of the common electrode are overlapped to divide the pixel electrode (12) into a plurality of areas. The openings (121, 122, 123) of the pixel electrode (12) and the openings of the common electrode are arranged alternatively. The first to sixth openings are formed parallel to each other in most of the areas except at the stem part (211) at the first opening (121), and at the fourth opening dividing the center of the pixel electrode (12), the branch end parts (213, 215) of the fourth opening that overlap the side of the pixel electrode (12), the horizontal end parts (222, 232), and the vertical end parts (223, 233) of the second and third openings.

The lower and upper polarizing plates (14, 24) are arranged so that their polarizing directions become respectively horizontal (0°) and vertical (90°) or vice versa.

Thus, the number of liquid crystal molecules rearranged by application of an electric field to lay down in a polarizing direction of the polarizing plates (14, 24) becomes smaller, and thus a texture appearance decreases. Also, since the liquid crystal molecules arranged by a fringe field are parallel to each other, movement of liquid crystal molecules is completed by a one-step operation. Therefore, response speed is very fast. In addition, openings extend largely in two directions in the pixel area, and these two directions form a 90° angle with each other. Also, since the openings of the upper and the lower plates are alternatively arranged, the direction of the fringe field is classified into 4 directions within one pixel area. Accordingly, a broad view angle can be obtained in all 4 directions.

Now, referring to Figs. 2 and 3, the structure of a thin film transistor array substrate for the liquid crystal display according to a second example of the present invention will be explained in detail.

According to the thin film transistor array substrate for a liquid crystal display of the second example, gate wiring comprising gate lines and gate electrodes connected with the gate lines are formed on an insulating substrate. A

semiconductor layer is formed on a gate insulating film covering the gate wiring, and on the semiconductor layer, data wiring comprising data lines crossed with the gate lines, source electrodes connected with the data lines, and drain electrodes opposed to the source electrodes around the gate electrodes are formed, and pixel electrodes
5 connected with the drain electrodes are formed thereon. The gate wiring or data wiring comprise a conductive film consisting of molybdenum and a molybdenum alloy comprising an additive material making a solid compound together with molybdenum.

Fig. 2 is an arrangement view of the thin film transistor substrate for a liquid
10 crystal display according to the second example of the present invention, and Fig. 3 is a cross-sectional view of the thin film transistor array substrate shown in Fig. 2 along the line V-V'.

On the glass substrate (110), gate wiring comprising a lower film (201) consisting of aluminum or an aluminum alloy with low resistance and an upper film
15 (202) consisting of molybdenum and a molybdenum alloy comprising an additive material making a solid compound together with molybdenum are formed with a tapered structure. The gate wiring comprises a gate line (121) extending horizontally, a gate pad (125) connected to the end of the gate line (121) and receiving gate signals from the outside to transmit it to the gate line, and a gate

electrode (123) of the thin film transistor connected with the gate line (121). Also, the gate wiring is overlapped with a conductive pattern (177) for a maintenance capacitor to be formed later and be connected with a pixel electrode (190) for improving a charge maintenance capacity of the pixel.

5 On the substrate (110), a gate insulating film (140) consisting of silicon nitride (SiN_x), etc. covers the gate wiring (121, 125, 123).

 On the gate insulating film (140) of the gate electrode (123), a semiconductor layer (150) consisting of a semiconductor such as amorphous silicon, etc. is formed, and on the semiconductor layer (150), ohmic contact layers (163, 165)
10 made of n⁺ hydrogenated amorphous silicon doped with a high concentration of silicide or n-type impurities are respectively formed.

 On the ohmic contact layers (163, 165) or the gate insulating film (140), data wiring consisting of a conductive film of molybdenum and a molybdenum alloy comprising an additive material making a solid compound together with molybdenum
15 are formed. The data wiring comprises a data line (171) formed vertically and crossed with the gate line (121) to define a pixel area, a source electrode (173) connected to the data line (171) and extending to the upper part of the ohmic contact layer (163), a data pad (179) connected to one end of the data line (171) and receiving picture signals from the outside, and a drain electrode (175) separated from

the source electrode (173) and formed on the ohmic contact layer (165) opposite the source electrode (173) around the gate electrode (123). Also, the data wiring is overlapped with the gate line (121) in order to improve maintenance capacity, and it may comprise a conductive pattern (177) for the maintenance capacitor to be
5 electrically connected with the pixel electrode (190) to be formed later.

The data wiring (171, 173, 175, 177, 179) may comprise a conductive film of aluminum or an aluminum alloy as does the gate wiring (121, 123, 125), and it may consist of a three-layered film of a conductive film of molybdenum alloy/ a conductive film of aluminum or aluminum alloy/a conductive film of molybdenum alloy.

10 On the data wiring (171, 173, 175, 177, 179), and on the semiconductor layer (150) that is not covered, a protection film (180) comprising an insulating film of a low dielectric insulating material comprising a Si : C : O : H or organic material with superior flattening properties and photosensitivity is formed. The protection film (180) may further comprise an insulating film consisting of silicon nitride, and in such
15 a case, the insulating film is preferably located under the organic insulating film and directly covers the semiconductor layer (150). Also, it is preferable to completely remove all organic insulating material from the pad part on which the gate pad (125) and a data pad (179) are located, which is particularly favorable with a COG (chip on glass) method liquid crystal display which directly installs gate operating integrated

circuits and data operating integrated circuits on a thin film transistor substrate in order to respectively transmit scanning signals and picture signals to the upper part of the gate pad (125) and data pad (179) of the pad part.

On the protection film (180), contact openings (185, 187, 189) respectively
5 exposing the drain electrode (175), the conductive pattern for the maintenance capacitor (177), and the data pad (179) are formed, and a contact opening (182) exposing the gate pad (125) together with the gate insulating film (140) is formed.

On the protection film (180), the pixel electrode (190) that is electrically
connected with the drain electrode (175) and is located on the pixel area, consisting
10 of the transparent conductive material IZO (indium zinc oxide) or ITO (indium tin oxide) etc., is formed. Also, on the protection film (180), a supplementary gate pad (92) and a supplementary data pad (97) that are respectively connected with the gate pad (125) and data pad (179) through the contact openings (182, 189) are formed.
The supplementary gate and data pads (92, 97) are to protect the gate and data
15 pads (125, 179), and they are not essential.

The above-explained thin film transistor substrate according to the second example of the present invention comprises a low resistance molybdenum alloy, and thus it can improve operation properties of a large screen liquid crystal display.

Now, referring to Figs. 4a to 7b and Figs. 2 and 3, a manufacturing process

of a thin film transistor array substrate according to the second example of the present invention will be explained in detail.

As shown in Figs. 4a and 4b, on the glass substrate (110), the lower film (201) consisting of a low conductive resistance material aluminum or aluminum alloy
5 and the upper film (202) consisting of molybdenum and a molybdenum alloy comprising an additive material making a solid compound together with molybdenum are sequentially laminated, and the upper film (202) and the lower film (201) are wet etched by a photo etching process using an aluminum etching solution (HNO_3 : H_3PO_4 : CH_3COOH : H_2O) to form gate wiring comprising a gate line (121), a gate
10 electrode (123), and a gate pad (125) with a tapered structure.

Next, as shown in Fig. 5a and 5b, three layers of a gate insulating film (140) consisting of silicon nitride, a semiconductor layer (150) consisting of amorphous silicon, and a doped amorphous silicon layer (160) are sequentially laminated, and the semiconductor layer (150) and the doped amorphous silicon layer (160) are
15 patterned by a patterning process using a mask to form a semiconductor layer (150) and an ohmic contact layer (160) on the gate insulating film (140) opposed to the gate electrode (123).

Then, as shown in Figs. 6a and 6b, a conductive layer comprising molybdenum and a molybdenum alloy comprising an additive material making a solid

compound together with molybdenum is laminated and patterned by a photo etching process using an aluminum etching solution to form data wiring comprising a data line (171) crossed with the gate line (121), a source electrode (173) connected with the data line (171) and extending to the upper part of the gate electrode (123), a data pad (179) connected to one end of the data line (171), a drain electrode (175) separated from the source electrode (173) and opposed to the source electrode (173) around the gate electrode (123), and a conductive pattern (177) for the maintenance capacitor, with a tapered structure.

Next, a doped amorphous silicon layer pattern (160) that is not covered with the data wiring (171, 173, 175, 177, 179) is etched to separate it into both sides around the gate electrode (123) and expose the semiconductor layer pattern (150) between doped amorphous silicon layers (163, 165). It is then preferable to treat with oxygen plasma in order to stabilize the surface of the exposed semiconductor layer (150).

Then, as shown in Figs. 7a and 7b, a low dielectric CVD film such as a Si : C : O film or a Si : O : F film, etc. is vapor-deposited by laminating silicon nitride, by coating an organic material with superior flattening properties and photo sensitivity on the substrate (110), or by PECVD (plasma enhanced chemical vapor deposition), to form a protection film. Also, the protection film is patterned together with the gate

insulating film (140) by a photo etching process using a mask to form contact openings (182, 185, 189, 187) exposing the gate pad (125), drain electrode (175), data pad (179), and conductive pattern for the maintenance capacitor (177).

Next, as shown in Figs. 2 and 3, a transparent conductive material is vapor
5 deposited and patterned by a photo etching process using a mask to form a pixel electrode (190) connected with the drain electrode (175) and the conductor pattern for the maintenance capacitor (177) through the contact openings (187, 185), and to form a supplementary gate pad (92) and a supplementary data pad (97) respectively connected with the gate pad (125) and the data pad (179) through the contact
10 openings (182, 189).

In addition, although this example of the present invention has been applied and explained for a manufacturing process for forming a semiconductor layer and data wiring by a photo etching process using different masks, the manufacturing process of the wiring according to the present invention can also be applied for a
15 manufacturing process of a thin film transistor array substrate for a liquid crystal display forming a semiconductor layer and data wiring by a photo etching process using one photosensitive film pattern in the same way in order to minimize manufacture costs.

In addition, the wiring comprising a conductive film consisting of

molybdenum and a molybdenum alloy comprising an additive material making a solid compound together with molybdenum can also be applied for a COA (color filter on array) structure forming a color filter on a thin film transistor array in the same way.

This will be explained in detail with reference to Figs. 8 and 9.

5 Fig. 8 is an arrangement view showing the structure of a thin film transistor substrate for a liquid crystal display according to the third example of the present invention, and Fig. 9 is a cross-sectional view along the line XXI-XXI' in Fig. 8.

Most of the structures are the same as those of Example 2. However, on the pixel area under the protection film (180), red, green, and blue color filters (R, G, B) having openings (C1, C2) exposing a drain electrode (175) and the conductive pattern for the maintenance capacitor (177) are formed vertically. The boundaries of the red, green, and blue color filters (R, G, B) are shown to be identical on the data line (171), but they can be overlapped with each other on the data line (171) so as to have function for blocking light let out from the pixel area, and they are not formed on a pad part on which gate and data pads (125, 179) are formed.

The protection film (180) on the red, green, and blue color filters (R, G, B) has contact openings (182, 189, 185, 187) exposing the gate pad (125), data pad (179), drain electrode (175), and conductive pattern for the maintenance capacitor (177) together with the gate insulating film (140). The contact openings (185, 187)

exposing the drain electrode (175) and the conductive pattern for the maintenance capacitor (177) are located inside the openings (C1, C2) of the color filters (R, G, B).

From the thin film transistor substrate for a liquid crystal display with a COA structure, the same effects as in Example 2 can be obtained.

5 Now, preferred embodiments of the nematic liquid crystal composition used for a liquid crystal material in the liquid crystal display of the present invention will be explained. However, the following Examples are to illustrate the present invention and the present invention is not limited to them.

Example 4

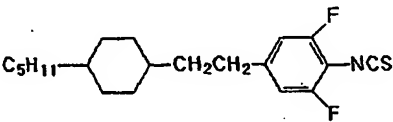
10 Phase transition temperatures of the compound of the above Chemical Formula 1 is shown in the following Table 1. In the Table 1, "m.p." indicates a temperature when the phase transition from crystal phase to liquid crystal phase or isotropic liquid phase occurs, and "c.p." indicates a temperature when the phase transition from liquid crystal phase to isotropic liquid phase occurs.

15 Birefringence (Δn) of a single liquid crystal compound was determined by extrapolation of measured birefringence of a 2-component mixed liquid crystal of 85 wt% of a mother liquid crystal 4-(4-hexylcyclohexyl)-1-isothiocyanatobenzene and 15 wt% of each compound shown in Table 1. Impurities were removed from each compound by distillation, column purification, and recrystallization, etc. to sufficiently

purify.

[Table 1]

No.	Molecular structural Formula	Phase transition temperature (°C)		Birefringence (Δn)
		m.p.	c.p.	
1		72.9	189.0	0.246
2		81.1	175.2	0.241
3		60.9	186.0	0.246
4		50.0	175.3	0.224
5		50.9	-	0.135
6		33.6	(20.1)	0.130
7		29.5	44.0	0.140

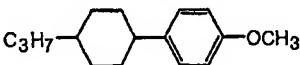
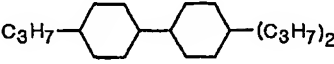
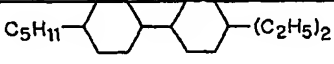
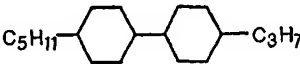
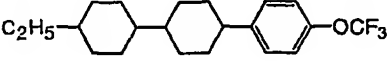
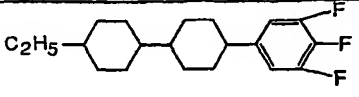
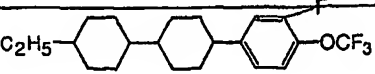
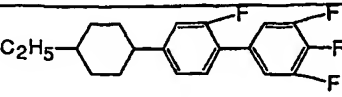
8		9.0	33.2	0.139
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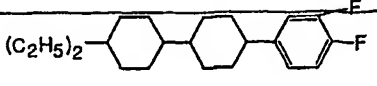
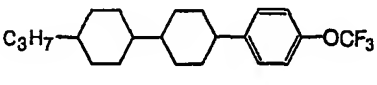
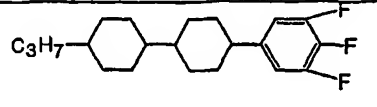
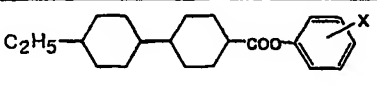
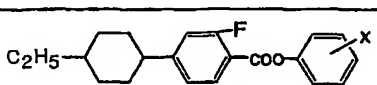
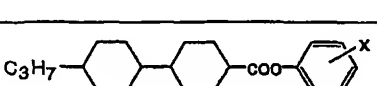
Comparative Example 1

A commercial mixture "TM1" comprised as shown in Table 2 was prepared

(TM1 = G1 + G2 + G3 + G4). Contents of each of G1 to G4 are based on wt%.

5 [Table 2]

	Compound	Contents (wt%)
G1		6.6
		5.4
		20.8
		5.36
G2		7.4
		7.0
		2.6
		3.97

G3		11.07
		3.5
		2.5
G4 (X is F)		3.4
		10.0
		10.4

The liquid crystal mixture TM1 in Table 2 is a presently-commercialized liquid crystal, and the response speed thereof was measured at 16.2 ms with a cell gap of 4.5 μm and the phase transition temperature was approximately 80 $^{\circ}\text{C}$.

5

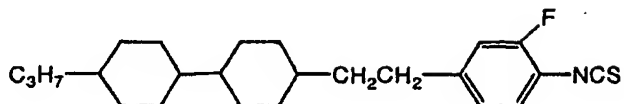
Examples 5 to 9

In order to confirm a change in physical properties corresponding to a high-temperature high-speed response, as a key material a compound of the following Chemical Formula 1a with a high phase transition temperature and large refractive anisotropy of the contents as shown in Table 3 and the balance of the mixed liquid crystal TM1 of Comparative Example 1 were mixed, and the phase transition temperature, refractive anisotropy, dielectric anisotropy, and response

10

speed (cell gap 3.75 μm) of the liquid crystal were measured, the results thereof being shown in Table 3.

[Chemical Formula 1a]



5 [Table 3]

	Contents (wt%)	T _{ni} (°C)	Δn	$\Delta\epsilon(20^\circ\text{C})$	Response speed (ms)
Comparative Example 1	-	80	0.0773	5.9	16.2
Example 5	7.3	88	0.0875	6.70	9.2
Example 6	13.2	94	0.0972	7.34	9.8
Example 7	17.1	98/102	0.1041(20°C)/ 0.09(28°C)	7.76(20°C)/ 6.01(28°C)	10.4
Example 8	20.6	101	0.1102	8.15	10
Example 9	26	108	0.1195	8.74	10.6
**denominator: measurement value example- 98°C (calculation value)/102°C (measurement value)					

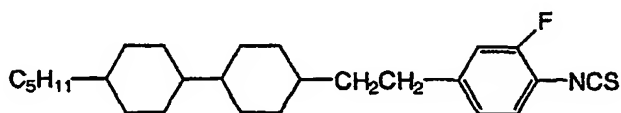
As shown in Table 3, Examples 5 to 9 obtained superior results compared to Comparative Example 1 (TM1) without a compound of the above Chemical Formula

1, and particularly, the response speed decreased to 65% and the phase transition temperature increased to 127%, which indicates that they are effective for high-speed high-temperature liquid crystal.

Example 10

5 This Example was conducted by the same method as in Example 5, except that 17 wt% of a compound of the following Chemical Formula 1b was used as the key material, and the phase transition temperature, refractive anisotropy, dielectric anisotropy, and response speed (cell gap 3.75 μm) were measured. The results are as follows.

10 [Chemical Formula 1d]



T_{ni}: 98 °C/104 °C, Δn : 0.1041/0.09(28 °C), $\Delta \epsilon$: /5.9(28 °C),

Response speed: 11 ms (28 °C)

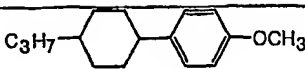
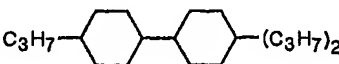
As seen from the results, compared to TM1, response speed decreased to 68% and phase transition temperature increased to 130%, which indicates that they are effective for high-speed high-temperature liquid crystal.

Comparative Example 2

A commercial mixture "TM1" comprised as shown in Table 4 was prepared (TM1 = G1 + G2 + G3 + G4). Contents of each of G1 to G4 are based on wt%.

[Table 4]

	compound	Contents (wt%)
G1		2.5
		2.6
		7.4
G2		7.0
		3.5
		3.97
G3 (X is F)		11.07
		3.4
		10.0
		10.4
G4		20.8
		5.36

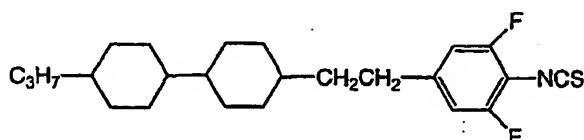
	6.6
	5.4

The liquid crystal mixture TM1 in the above Table 4 is a presently-commercialized liquid crystal, and the response speed thereof was measured at 16.2 ms at a cell gap of 4.6 μm and the phase transition temperature was about 80 $^{\circ}\text{C}$.

Examples 11 to 15

In order to confirm a change in physical properties corresponding to a high-temperature high-speed response, as a key material, a compound of the following Chemical Formula 1c with a high phase transition temperature and a large refractive anisotropy of the contents as shown in Table 5, and the balance of the mixed liquid crystal TM1 of Comparative Example 2 were mixed, and the phase transition temperature, refractive anisotropy, dielectric anisotropy, and response speed (cell gap 3.77 μm) were measured according to % concentration, results thereof being shown in Table 5.

[Chemical Formula 1c]



[Table 5]

	Contents (wt%)	T _{ni} (°C)	Δn	$\Delta\epsilon(20\text{ }^{\circ}\text{C})$	Response speed (ms)
Comparative Example 2	-	80	0.0773	5.9	16.2
Example 11	7	87	0.0866	6.7	9.1
Example 12	14	93	0.0982	7.5	9.7
Example 13	16.7	96/98.3	0.1027(20°C)/ 0.0907(28°C)	7.8(20°C)/ 6.7(28°C)	10.2
Example 14	20	99	0.1082	8.2	10.3
Example 15	26	104	0.1181	8.9	10.7
**denominator: measurement value, Example 96°C (calculation value)/98.3°C (measurement value)					

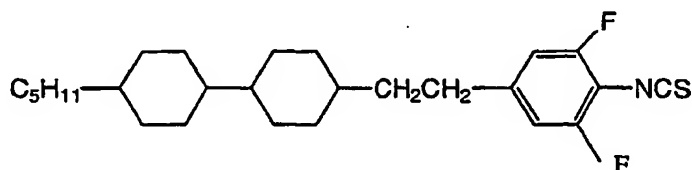
As shown from the Table 5, Examples 11 to 15 obtained superior results compared to Comparative Example 2 (TM1) without a compound of the above Chemical Formula 1, and particularly, response speed decreased to 60% and phase transition temperature increased to 122%, which indicates that they are effective for high-speed high-temperature liquid crystal.

Example 16

This example was conducted by the same method as in Example 5, except

that 17 wt% of the compound of the following Chemical Formula 1d was used as the key material, and phase transition temperature, refractive anisotropy, dielectric anisotropy, and response speed (cell gap 3.86 μm) were measured. The results are as follows.

5 [Chemical Formula 1d]



T_{ni}: 98 °C/100 °C, Δn : 0.1003/0.0908(28 °C), $\Delta \epsilon$:

7.7(20 °C)/6.2(28 °C),

Response speed: 11 ms (28 °C)

10 As seen from the results, compared to TM1, the response speed decreased to 68% and phase transition temperature increased to 130%, which indicates that the compositions of the present invention are effective for a high-speed high-temperature liquid crystal. So far reported NCS mixtures have phase transition temperatures, refractive indexes, and response speeds of T_{ni}: 71 °C, Δn : 0.15, response speed:

15 14.6 ms, and in this case, they have low phase transition temperatures and high refractive indexes and thus are difficult to be made into products, and in the case they have T_{ni}: 95 °C, Δn : 0.089, and response speed: 21.3 ms, the response speed is slow and thus they are difficult to use for a high-speed high-temperature liquid crystal

corresponding to moving pictures. Meanwhile, Examples of the present invention simultaneously satisfy high speed and high temperature characteristics corresponding to moving pictures and thus they are likely to be made into products.

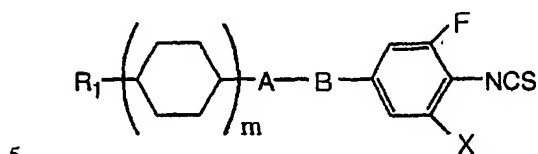
Although preferred examples of the present invention have been explained
5 in detail, the scope of the right of the present invention is not limited to them, and various modifications and improvements can be made by an ordinarily skilled person within the basic idea of the present invention defined in the following claims.

As explained, the nematic liquid crystal compound of the above Chemical Formula 1 or Chemical Formula 2 according to the present invention and a liquid
10 crystal composition comprising the same have a large birefringence (Δn), elasticity coefficient, and dielectric anisotropy (Δ), low threshold voltage (V_{th}) and viscosity, a broad operating nematic phase temperature range, a high voltage holding ratio (VHR), and superior chemical stability. In addition, they have high phase transition temperatures compared to a commercial-mixed liquid crystal and can realize a high
15 response speed, and thus if they are used for twist nematic (TN) or super twist nematic (STN) liquid crystal displays, or for active matrix (AM) method thin film transistor (TFT) liquid crystal displays, afterimage and crosstalk can be improved, and they are effective for making response speed high and lowering operation voltage because they have low viscosity and a large elasticity coefficient.

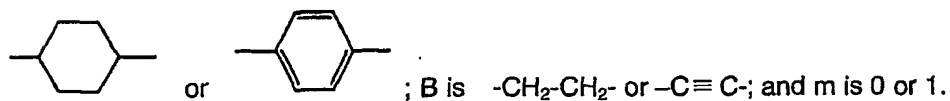
WHAT IS CLAIMED IS:

1. A nematic liquid crystal composition comprising a nematic liquid crystal compound represented by the following Chemical Formula 1:

[Chemical Formula 1]



wherein R_1 is $C_nH_{2n+1}O$, C_nH_{2n+1} , or C_nH_{2n-1} (n is 1~15); X is H or F; A is

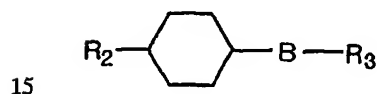


2. The nematic liquid crystal composition according to Claim 1, comprising

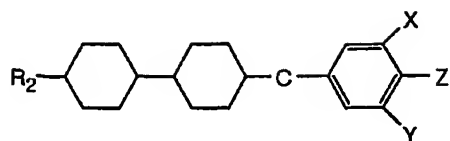
a) 2 to 80 wt% of the nematic liquid crystal compound represented by the
10 above Chemical Formula 1; and

b) 20 to 98 wt% of at least one kind of liquid crystal compound selected from
a group consisting of compounds represented by the following Chemical Formula 6,
Chemical Formula 7, and Chemical Formula 8:

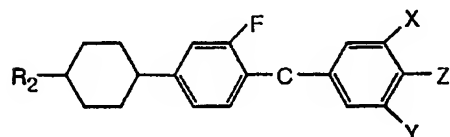
[Chemical Formula 6]



[Chemical Formula 7]



[Chemical Formula 8]



wherein R_2 and R_3 are independently or simultaneously a C1-15 alkyl group

5 or alkoxy group; B is phenyl or cyclohexyl; C is a single bond $-CH_2CH_2-$ or $-COO-$; X and Y are independently or simultaneously a hydrogen or fluorine atom; and Z is a hydrogen, $-OCF_3$, or a fluorine atom.

3. A liquid crystal cell for a liquid crystal display wherein the nematic liquid crystal composition of Claim 1 is injected between two glass substrates or plastic substrates
10 as liquid crystal.

4. The liquid crystal cell for a liquid crystal display according to Claim 3, wherein the liquid crystal cell comprises transparent electrodes making up pixels inside the substrates, and an orientation film for orienting liquid crystal molecules toward one direction on the transparent electrodes.

15 5. The liquid crystal cell for a liquid crystal display according to Claim 3, wherein the liquid crystal cell has a phase transition temperature of at least $85\text{ }^{\circ}\text{C}$ and a response speed of 9 to 11 ms.

6. A liquid crystal display comprising the nematic liquid crystal composition of Claim

1.

7. A liquid crystal display comprising

a first substrate having an outside and an inside;

5 a second substrate opposed to the first substrate and having an outside and
an inside;

pixel electrodes formed on one of the insides of the first substrate and the
second substrate;

common electrodes formed on one of the insides of the first substrate and
10 the second substrate; and

liquid crystal cells wherein the nematic liquid crystal composition of Claim 1
is injected between the first substrate and the second substrate as liquid crystal.

8. The liquid crystal display according to Claim 7, wherein 1-gradation voltage
applied between the pixel electrode and the common electrode has a value of a the
15 range within which a standardized comparison ratio becomes 0.8 or more in every
view angle, when standardizing a comparison ratio for applying 0V of 1-gradation
voltage as 1.

9. The liquid crystal display according to Claim 7, wherein the display further
comprises a first polarizing plate arranged outside of the first substrate and a second

polarizing plate arranged outside of the second substrate, and the polarizing axes of the first polarizing plate and the second polarizing plate are perpendicular to each other.

10. The liquid crystal display according to Claim 7, wherein the liquid crystal is
5 oriented perpendicularly to the first and the second substrates when voltage is not applied between the pixel electrode and the common electrode.

11. The liquid crystal display according to Claim 7, wherein the pixel electrode and the common electrode further comprise domain-regulating means for regulating an inclined direction of the liquid crystal, formed on at least one side of the first substrate
10 and the second substrate.

12. The liquid crystal display according to Claim 7, wherein the liquid crystal display comprises

an insulating substrate;

gate wiring formed on the insulating substrate and comprising gate lines and

15 gate electrodes connected to the gate lines;

a gate insulating film covering the gate wiring,

a semiconductor layer formed on the gate insulating film;

data wiring formed on the semiconductor layer and comprising data lines

crossed with the gate lines, source electrodes connected to the data lines, and drain

electrodes opposed to the source electrodes around the gate electrodes, and

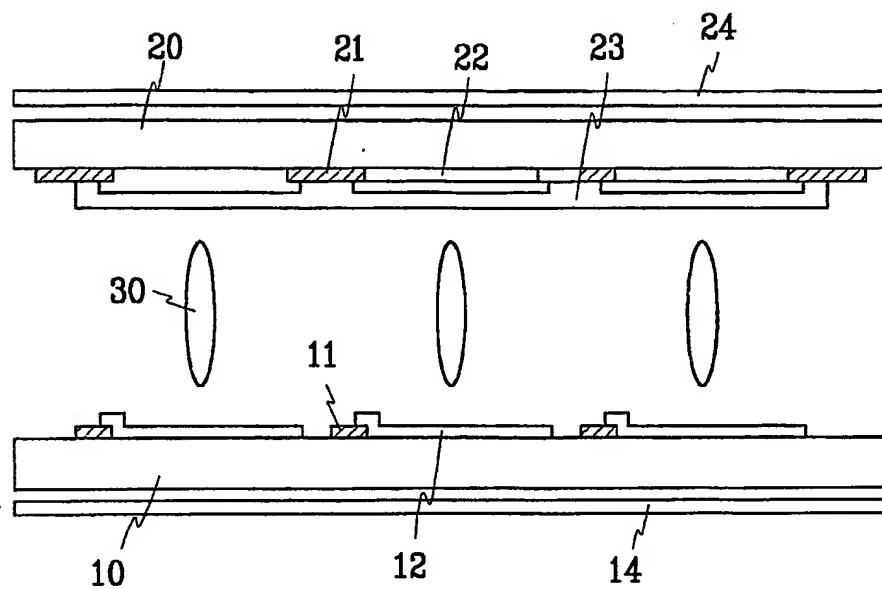
a thin film transistor array substrate comprising pixel electrodes connected to the drain electrodes as the second substrate.

13. The liquid crystal display according to Claim 12, wherein the thin film transistor
5 array substrate further comprises a protection film formed between the data wiring and the pixel electrodes.

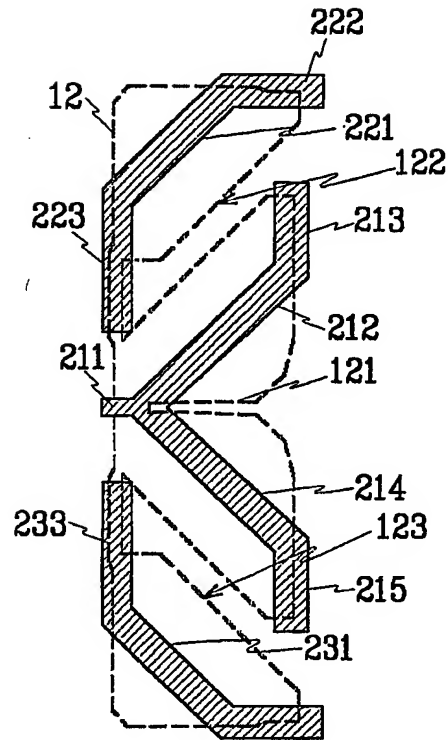
14. The liquid crystal display according to Claim 12, wherein the thin film transistor array substrate further comprises an ohmic contact layer formed between the semiconductor layer and the data wiring, being doped with a high concentration of
10 impurities.

15. The liquid crystal display according to Claim 12 further comprising red, green, and blue color filters formed on the gate wiring and the data wiring.

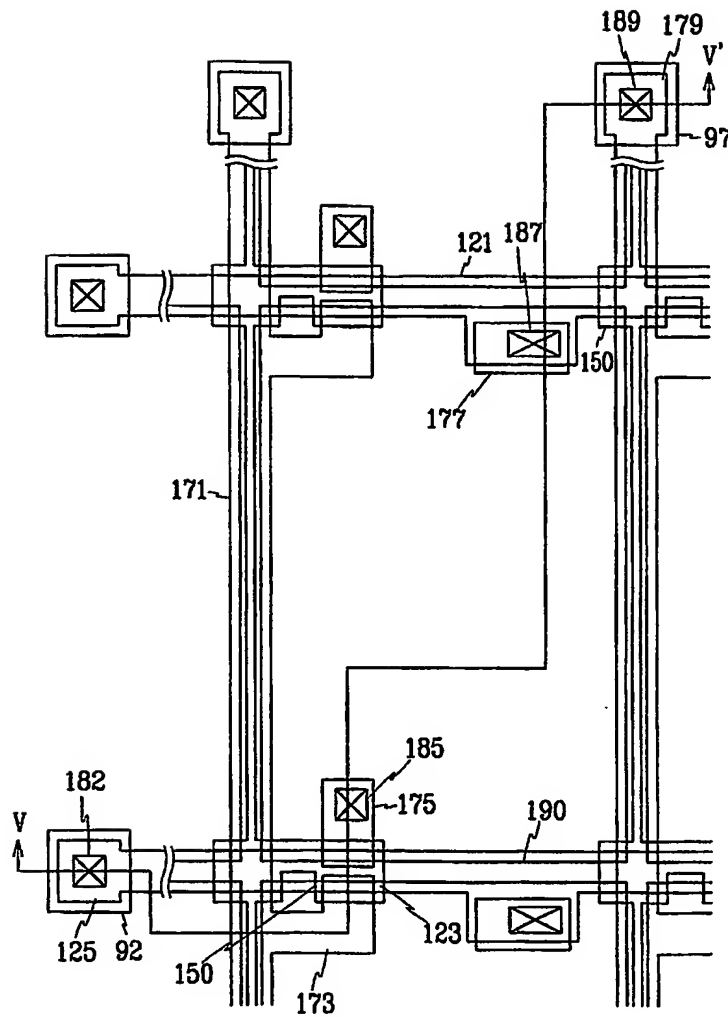
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FIG.1A



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FIG.1B

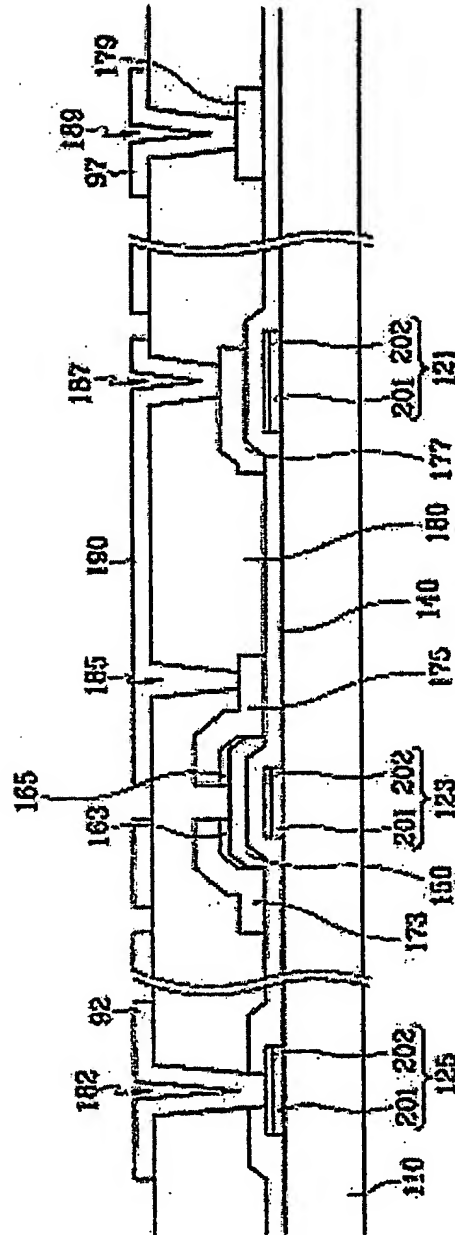


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FIG. 2

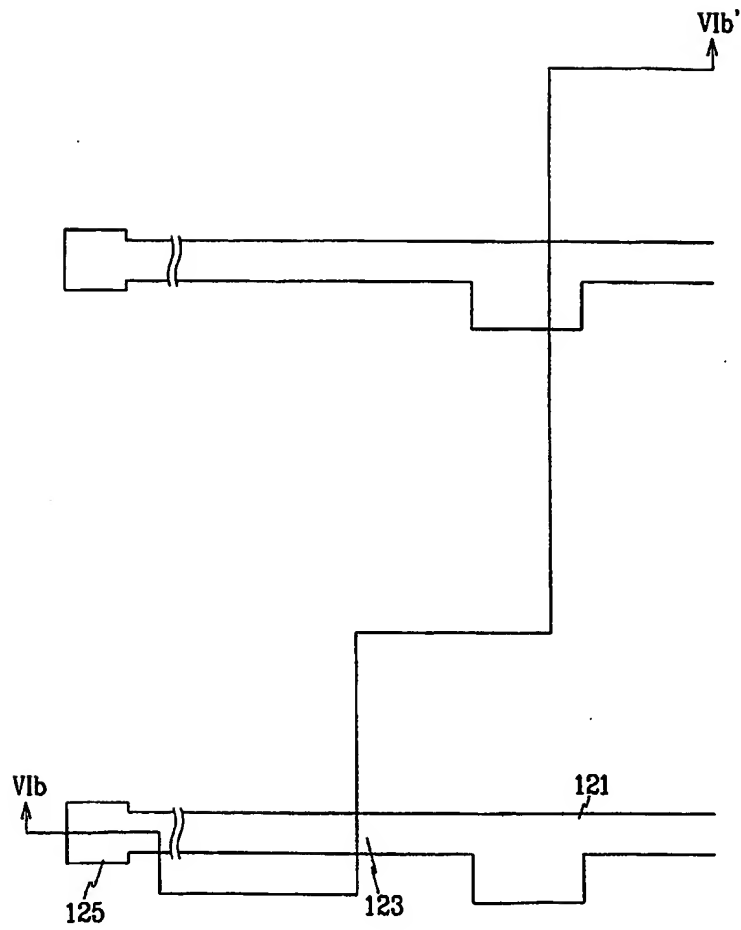


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FIG. 3

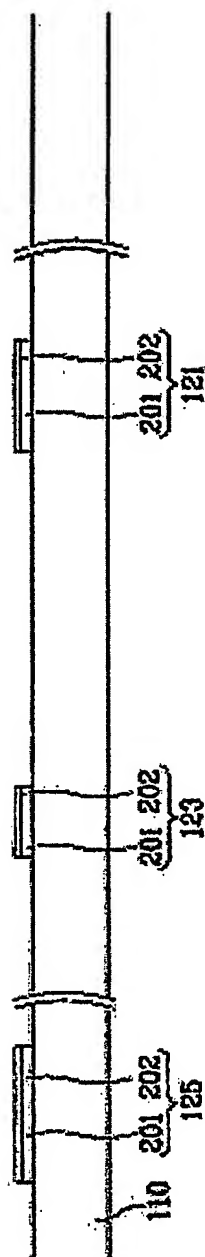


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FIG. 4A

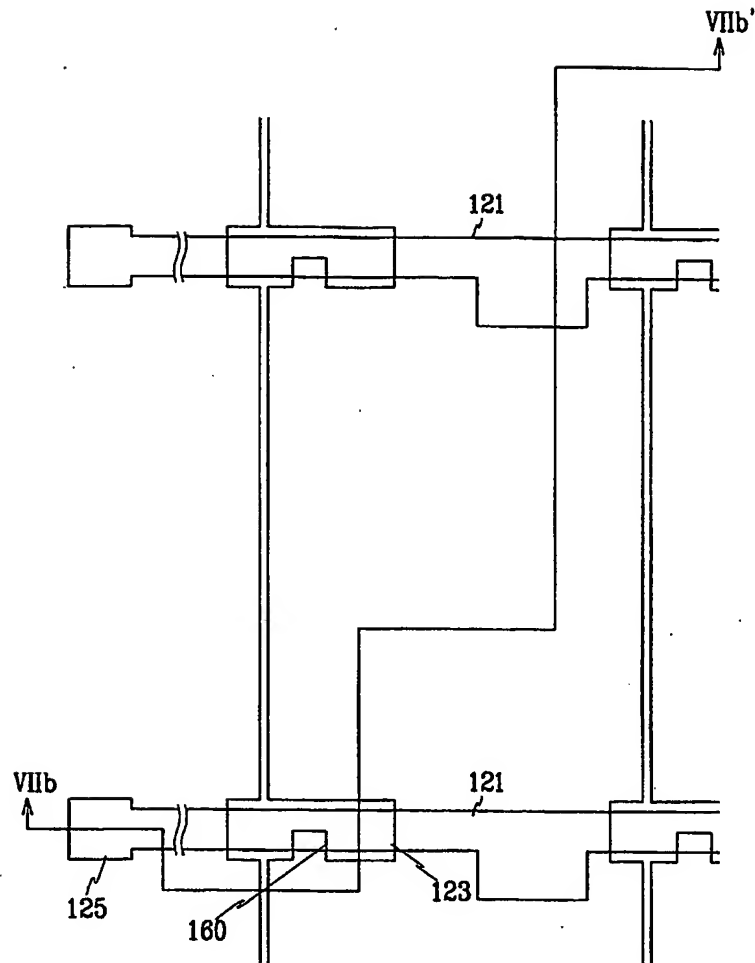


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FIG.4B

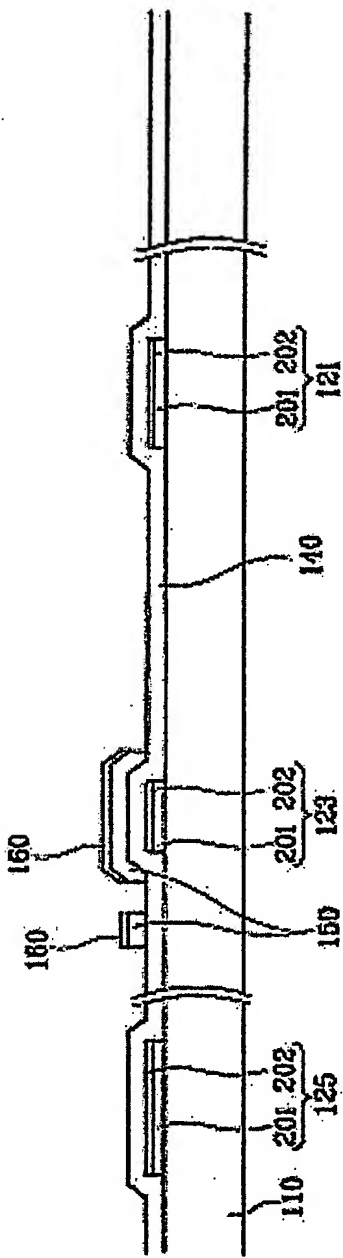


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FIG. 5A



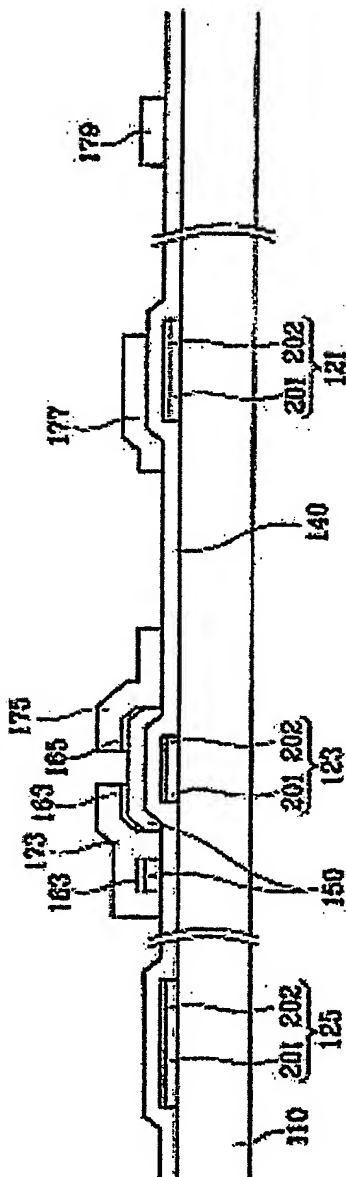
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FIG. 5B



The diagram illustrates a semiconductor device structure. The top portion is a cross-sectional view showing a substrate with a gate stack (121) and a source/drain region (171). The bottom portion is a plan view showing a gate stack (125) and a source/drain region (173). The device includes a gate stack (121, 125), a source/drain region (171, 173), and a gate stack (123). The device is labeled with various reference numerals: 121, 123, 125, 150, 171, 173, 175, 177, 179, 190, and VIIIb, VIIIb'.

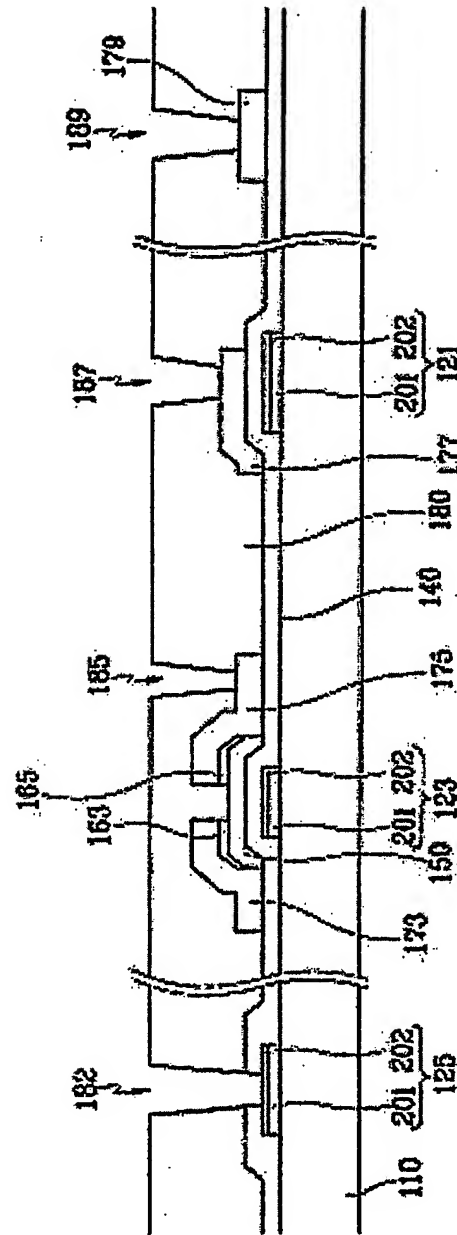
FIG. 6B



[illegible]

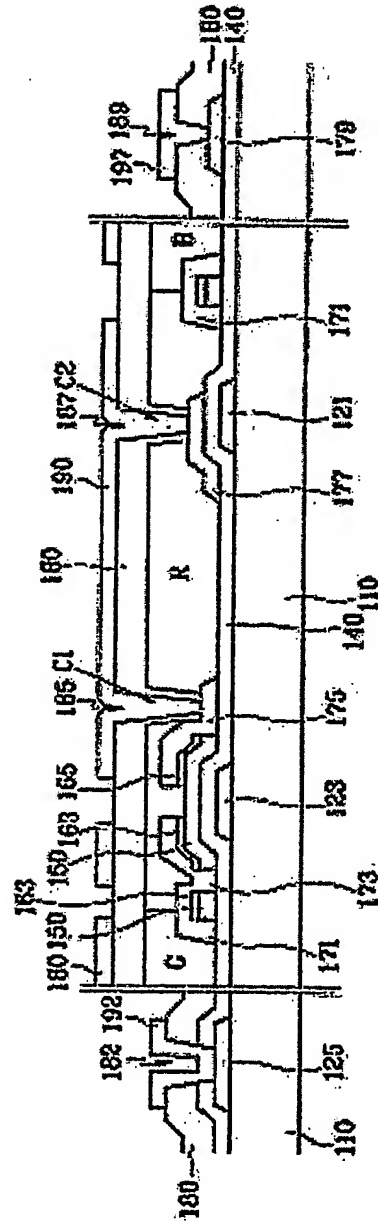
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FIG. 7B



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FIG. 9



INTERNATIONAL SEARCH REPORT

International application No.
PCT/KR 03/01476-0

CLASSIFICATION OF SUBJECT MATTER		
IPC ⁷ : C09K 19/04, 19/30,		
According to International Patent Classification (IPC) or to both national classification and IPC		
B. FIELDS SEARCHED		
Minimum documentation searched (classification system followed by classification symbols)		
IPC ⁷ : C09K		
Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched		
Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)		
WPI, EPODOC Databases, REGISTRY and CAPLUS Databases		
C. DOCUMENTS CONSIDERED TO BE RELEVANT		
Category	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
P,X	WO 03/035798 A1 (SAMSUNG ELECTRONICS CO., LTD.) 1 May 2003 (01.05.03) <i>the whole document, espl. pages 11-14, claims.</i>	1,2,5,6
P,X	JP 2003 129052 A2 (DAINIPPON INK AND CHEMICALS, INC.), 8 May 2003 (08.05.03), (abstract) CA [online], Chemical Abstracts online [retrieved on 11.9.2003]. Retrieved from: CAPLUS Database, STN-International, Karlsruhe (DE), CAPLUS Accession no. 2003:349562 and Computer-translation of JP 2003129052 A2, retrieved via the Internet. Retrieved from the Japanese Patent Office on 11.9.2003, URL < http://www4.ipdl.jpo-miti.go.jp/cgi-bin/tran_web.cgi_ejje?N0000=60&N0120=01&N2001=2&N3001=H2003-129052 >; <i>CAPLUS and PAJ abstracts and Computer translation of the Patent claims.</i>	1,3,6
<input checked="" type="checkbox"/> Further documents are listed in the continuation of Box C. <input checked="" type="checkbox"/> See patent family annex.		
* Special categories of cited documents: „A“ document defining the general state of the art which is not considered to be of particular relevance „E“ earlier application or patent but published on or after the international filing date „L“ document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified) „O“ document referring to an oral disclosure, use, exhibition or other means „P“ document published prior to the international filing date but later than the priority date claimed „T“ later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention „X“ document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone „Y“ document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art „&“ document member of the same patent family		
Date of the actual completion of the international search 11 September 2003 (11.09.2003)		Date of mailing of the international search report 20 October 2003 (20.10.2003)
Name and mailing address of the ISA/AT Austrian Patent Office Dresdner Straße 87, A-1200 Vienna Facsimile No. 1/53424/535		Authorized officer WENIGER S. Telephone No. 1/53424/341

INTERNATIONAL SEARCH REPORT

International application No.

PCT/KR 03/01476-0

C (Continuation). DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 5578241 A (H. PLACH et al.) 26 November 1996 (26.11.96) <i>the whole document, esp. claim and columns 6-8.</i>	1,2
A	<i>the whole document, esp. claim and columns 6-8.</i> ---	3-15

INTERNATIONAL SEARCH REPORT

International application No.
PCT/KR 03/01476-0

Further remarks:

1) The search has been limited to those parts of the claims which - from the chemical point of view - find sufficient support by the disclosure of the application, namely to compositions comprising a compound of formula 1 with:

- A being cyclohexyl exclusively and
- B being CH₂-CH₂;

2) the meanings of the "R1" residue is not clear, since by the giving of the molecular formula, only, the residues are not characterized sufficiently, because for examples it cannot be verified which bonds are comprised in these residues;

INTERNATIONAL SEARCH REPORT

Information on patent family members

International application No.

PCT/KR 03/01476-0

Patent document cited in search report			Publication date	Patent family member(s)	Publication date
JP	A	20031290 52A2		none	
US	A	5578241	1996-11-26	none	
WO	A	35798		none	